

LONG ISLAND GROUP Sinclair TIMEX

L.I.S.T.ING

December January

SPECIAL ISSUE

\$1.50

FLASH! FLASH! FLASH!

MICRODRIVES WORK!

Port Jefferson, N.Y. - Dec. 18, 1984. Nazir P. has successfully interfaced Sinclair Microdrives to his 2068 (with Emulator). He used a homebrew adapter board which simply rerouted the different pins for the various buss lines from his 2068 to their proper ZX Spectrum position (see back issues of LIST).

The only other changes, which may not have been required (experimentation is still going on) were the inclusion of a +12 V. (7812) regulator in place of the Spectrum RAM 12 V. (there is only a 16 V capacitor in the Spectrum Power Supply and the Timex Supply can go as high as 20 V - no load) and a -5 V supply. Both of these may actually only be required for the RS232 port.

Right now, IO REQGE and ROMCS are not hooked up either, but with no apparent problems.

Nazir also tells us that both Zebra and Doug Dewey are working on 2068-to-Spectrum bus adaptors.

LIST - DATA BASE

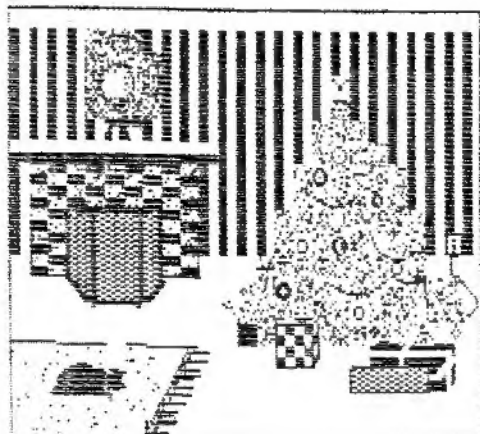
We'd like to set up special interest groups (e.g., at least 20% of our members are TS1000 only owners) and find out more about our group resources.

Here's our member questionnaire again. Even if you filled one out the first time (July), your situation has probably changed. Please take the time to fill out the questionnaire and return it to LIST, Box 438, Centerport, N.Y. 11721-0438.

We'll also need a volunteer to enter the data into PRO/FILE.

LIST GROUP

P.O. BOX 438
CENTERPORT, N.Y. 11721-0438



Holiday Greetings
Tom Green

MEETING DATE CHANGED

Last minute problems have forced us to change the January meeting date to January 6, 1984, still at 9 Dartmoor, Northport - 2:00PM.

LAST MINUTE NEWS

Nazir's Spectrum, Microdrive and Interface I came with 5 tapes (Games & Education), TASWORD II and a utility called "copier." This last, supplied by Sinclair, allows you to transfer programs from tape to microdrive. So far, no "protected" software has caused a problem.

The LAST TWO PAGES of this Special edition, will contain some very special information for our members.

Finally, TOM Bent, of Syncware News tells us that John Oliger is working on a simple hardware modification which may enable some of the Spectrum "unrunnables" to run with our emulators and issue III ROM's. His theory involves Keyboard scanning routines and hardware. To test it out, he needs samples of those "unrunnable" programs. If you have one you can't run on your emulator - send it to John for testing. John's address is 11601 Whidbey Drive, Cumberland, IN 46229,

L.I.S.T.ING

MEETING NOTES - DECEMBER 3, 1984

I. NEXT MEETING(S)

- a) Sunday Jan. 6th at 9 Dartmoor - 2:00PM
- b) Sunday Feb. 3rd - South Shore - 2:00PM?
(Basic for Beginners Class - 1:00PM??)

Theme of the meeting - Word processors and 64 column mode. Bring your WP software for a demo. Zebra Systems will be demoing some Graphics tablet software for technical drawing.

II. The meeting was called to order at 2:15PM. New members Izzy G. and Com B. swelled our ranks of active members to 40. A small, year end "dividend" still looks likely. Infoworld magazine published a short blurb about us in their Dec. 10th issue. We are receiving some 10 pieces of mail, requesting information, each day. A plea was made for a corresponding secretary to help handle this workload. A volunteer will step forward at the next meeting.

Bob M. has begun to enter the member data base info. The first version, using PRO/FILE, is ready.

It was further suggested that a software list be generated. This would allow a member to talk to someone who already owns software he is thinking about buying, before he jumps in head first. Bob wasn't at the meeting, but will be asked to coordinate this activity.

Alternate meeting dates, times and places (e.g., schools, libraries) were discussed, with the consensus being that the present method was still adequate for our needs.

Suggestions for community service activities (above and beyond our provision of free information and newsletters to all who apply) were solicited. BASIC for Beginner classes were discussed.

Members will bring their suggestions for software to be purchased for a true "members only" software library, next meeting. No rental fee would be charged to members. A small handling fee would be required (e.g., for insurance, mailers etc.) and a non-copyright infringement agreement must be signed. (Do we have an attorney out there who would write one up for us?) The purpose of the library will be to let you "try before you buy". A volunteer for full time "librarian" is absolutely essential for this plan to work. If none is found, next meeting, the idea will be scrapped.

III. NEW NOTES AND ANNOUNCEMENTS

a) Nazir had the following:

- 1) Abersoft FORTH works with Spectrum ROM
- 2) PASCAL (Hi-Soft) works
- 3) 'C' Computer (') - bombs out
- 4) Floating Point Compiler Loads but won't ~~run~~
- 5) & Hi-Soft Toolkit " " "
- 6) Betasoft BASIC - works very well
- 7) Hot Z is available on Cartridge, Using an Olliger Board-\$20.00 and ROM's from Hot Z (Ray K). This system comes up in the dock bank and therefore uses virtually no RAM in your 2068.

- b) Bob Gilder demoed a Spectrum he had just bought in Europe. He ran it on a B & W set by adjusting the H & V frequency controls. Color is another matter though. Bob also showed us an issue of "Spectrum Computing" a tape magazine. It was good, but Bob feels it is not up to the standard set by 16/48 magazine.
- c) Jeff S. and Stewart N. demoed Zebra's "Coloring Book" software for the graphics tablet. A dozen or so line drawings are provided on each \$10 tape. The (young) user uses the tablet to fill in the different picture areas.

Zebra will be featured in an upcoming segment of "High-Tech Times" on PBS. Check your local channel for show #210. It will be on in our area in late December and on WNET (13) in March or April '85.

A 4 page addendum to Smart Term II software has been published, Stewart will bring samples to the next meeting. Zebra's BBS will continue to be down 9-5 weekdays until the First of the year. TS 1000 software for the Westridge MODEM is due out soon.

- d) Heniz K. (2068) and Paul D. (TS 1000) have sent messages and pseudo REM statements between their Byte Back NO DEMS. An attempt to do so again, at the meeting, failed, probably due to the lack of a real current loop.
- e) Hepple called LICA's BBS and we used his EP-44 to get hard copy. What was particularly interesting, was that the 2068's and EP-44's buffers worked in tandem.
- f) Paul D. showed his Kempston joystick interface (see article) for the "Spectrum".
- g) Library tapes were distributed to each Loop. Remember to keep the tape no more than 1 week if possible, to add a program of your own, and to send them off to the next "loopmate". Tape #2 is already in the works.

IV. LICA NOTES

LICA meets 3rd Friday of each month at 8:00PM - at NYIT - Old Westbury Campus

Meeting dates and Subjects:

Jan 18	AT&T, BULLETIN BOARDS
Feb 15	FENCEPT
Mar 15	BETTER BASIC, NEWSDAY (CNN)
Apr 19	COMPUSEIVE

If you are a LICA member, as well as a LIST member, please let us know.

SPECIAL NOTES:

1. This Special Holiday issue will cover both Dec. and Jan. We think you'll agree that both size and quality of articles make this the equivalent of 2 regular issues.

2. Election of Officers

Same normal membership year is Feb. to Jan., we should elect new officers and committee chairmen no later than the Feb. meeting. Please be prepared to make nominations. Positions which must be filled are:

- President
- Sec'y - Treas.
- Corresponding Sec'y
- Newsletter Editor
- Librarian
- Membership coordinator (Data-Base)

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"Beep", I'm Done

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Graphics
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LISTING Policy:

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Your reviews, programs, comments, hardware projects, etc., are eagerly solicited for publication in LISTing.

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Please note our new address - P.O. BOX 438, Centerport, N.Y. 11721-0438
Mail sent to the old address must be forwarded there and will take longer to reach us.

NOTE: PARTIAL YEAR MEMBERSHIPS AVAILABLE

Normal membership year is Feb. through Jan. at a cost of \$12.00.
By keeping as many members as possible on that basis, we keep our costs and chances of error, down.

If you wish to begin subscribing later in the year, please sign up for the end of this year and all of next. E.g., to subscribe in Nov. 84, we ask that you remit \$18.00, which will cover you through Jan '86, rather than send 4.00 now and 14.00 in Jan. (the rates may go up then also)

We will accept partial years or different subscription runs, on a limited basis (particularly from members outside the U.S.). But, please be aware that addition to possible rate increases, your "account" must be handled "by hand" and errors may occur. More on international members next issue.

PRINTING:

We have received a number of complaints about size and legibility of the newsletter. Every effort is being made to correct these problems. We hope the size of print in this issue is more acceptable.

SPECIAL THANK YOU:

To Chuck Russell, he produced last months newsletter, and the library tapes. Great work Chuck!

VENDOR REPORT - CATALOGS RECEIVED

Syntax Software
PO Box 486
West Kennebunk, Me 04094

JMJ Distributors
Box 208
Hewlett, N.Y. 11557
(516) 825-2650

Compucon
62 Fulton Avenue
Saithtown, N.Y. 11787
(516) 724-5975

National Software Library
200 Mulgrave Road
Chesham, Surrey SN2 6JT UK

Software Supermarket
87 Howard's Lane
London, England SW15 6NU

Zebra Systems
78-06 Jamaica Avenue
Woodhaven, N.Y. 11421
(718) 296-2383

Aerco
Box 18093
Austin, Texas 78760
(512) 451-5874

Sharp's
127 Nine Mile Road
Sandston, Va. 23150
(804) 737-4895

Kaltek
Box 971, Adjuntas
Puerto Rico, 00601

Kompak, Inc.
1525 Aviation Blvd
Suite A-111
Redondo Beach, Ca 90278

Ramex
48945 Van Dyke Road
Utica, MI 48087
(313) 463 1795

VENDOR REPORT

Some Timex Software
They don't say what

A buying service
May provide discounts
on peripherals

Good Prices on Diskettes
(Bulk = 100/box)

Tape rentals & good prices (discount)
on Purchases-and good service.

Most "Hit" software titles
Prices are high (LIST)
Service is good. Takes VISA

New Graphics Tablet - 2068
sells most 2068/TS1000 software
and Peripherals

Centronics interface - \$99 for TS1000
also Disc Drives

Strategy Games
Disassembler etc.
Takes Visa

Compuuser 75136.1775 - MC1 213 9544

Simple hardware interfaces
Most less than \$25 for the TS1000.

Eprom "Cartridge"Boards
for TS1000

Most 2068 Software including TAS, word
Pro/file and more-List price - takes Visa

Softsync
14 East 34th Street
NYC10016
(212) 685-2080

Sunset Electronics
2254 Taraval Street
San Francisco, Ca. 94116
(415) 665-8330

Games to Learn By, Inc.
PO Box 78
28 Claire Hill Road
Collinsville Ct. 06022
(203) 673 7089

Heath Computer Services
950 East 32nd South
Greentown, In. 46936
(317) 628 3130

E. Arthur Brown
3405 Ravnee Drive
Alexandria, Mn 56308
(612) 762 8847

Wilco Services
PO Box 5696
Vandenberg, Ca 93437-5696

LMJ Data Systems
4 Butterfly Drive
Hauppauge, N.Y. 11788

Pleasantrees
PO Box 2034
Mesa, Az 85204

Barry Carter
Box 614
Warren, MI 48090

Best Business & Tax Service
Box 1586
Vista, CA 92083
619-727-4012

HD Software
1445 Oldfield Road.
Decatur, GA 30030

2068 Games & Zeus assembler
Takes Visa/M/C

Most all hardware/software, books
for 2068 and 1000
List price or below. Some Spectrum Software.

Carry most other vendors lines of TS1000
and 2068 materials

Person's Business Programs

TS1000 & 2068 -Takes Visa

Most hardware & software. List Price,
Takes Visa

Rampacks - \$11
M.W. Printer - \$30
TS 1000 SW - 50 Min Order \$50.

New TS Publication?
Quarters
Sells TS1000 & TS 2068 Software

Graphics "AT02"
XON?
New 2068 Software due soon

30 page manual
to help you use M-Term - \$5.00

1984 Tax Program
100/2068 \$29.95

Coupon Manager

\$24.95 for TS 2068

JOY TO THE WORLD!

HARDWARE REVIEW

ITEM: KEMPSTON JOYSTICK INTERFACE
FOR: SPECTRUM (TS2068 & TS1000 Two!)
FROM: KEMPSTON MICRO ELECTRONICS LTD.
SINCE WAY
MOBURN ROAD INDUS. ESTATE
KEMPSTON, BEDFORD MK42AF-UK
PRICE: £11.50 (incl. post)

By now, if you've converted your TS 2068 to a Spectrum, you've found out that your built-in joystick ports cannot be used with Spectrum Software. While many strategy and adventure games and utilities benefit little from the use of a joystick, there are arcade games (ATC ATAC comes to mind) which are virtually impossible to play without one. The ZX Spectrum didn't originally come with a joystick port, so the enterprising British firm of Kempston Inc. invented one. The Kempston joystick caught on so well in the U.K. that most new software is compatible with their interface. The question, of course, is whether or not this interface will work with a TS 2068 which has been "Spectrumized". The answer is a resounding "YES!"

The Kempston unit is a small, simply designed single-sided circuit board mounted in a black plastic case. The assembled unit measures about 2" X 1" X 3 1/2" wide and fits on the expansion bus of your 2068, albeit snugly. If you're worried about straining your 2068 PC board, I'd remove it from its case (4 screws). Mounted on the PC board are two simple logic chips, one selects input port 31, the other "exposes" the 2068's data lines to the contacts in your joystick. When you close one of those, a data line is taken high.

Any Atari compatible joystick, with 9 pin 'D' plug, can be plugged into the port on the Kempston joystick. That port's mounting however, leads to what is perhaps the most prevalent criticism of the Kempston system. The port is on the front of the unit and this means your joystick's cord must trail across your keyboard for use. On complicated games, (like flight simulators) where you must use many other keys for control functions, status reports, etc., that trailing cord can be a bother.

The board is dead ended. That is, it has a 2 X 23 way female socket for the back of your computer, but no male edge connector for more add-ons. As long as the joystick is your last peripheral and the front mounted connector doesn't interfere with the peripheral in front of it, you're OK. The Timex printer interface works fine, others might not.

Last criticism is one I've voiced before. That is, the lack of decoding and decoding documentation. The Kempston ads don't tell you how the joystick is mapped, and they should. There is a small flyer which comes with the unit, and gives some good examples of how to use the stick in your program and/or patch commercial software. (The commercial program must first be "broken" and then disassembled, however, which is no mean feat). IN 31 is the command, we're told, which will activate the unit. If a 0 is returned, then nothing has happened. The four directions and fire return 1,2,4,8 and 16 to the IN 31 command. A look at the actual circuit shows that, in addition to IN 31 and RD, only address line A5 is used to activate the interface. This means the unit responds to any I/O port in the range:

255 to 64 & 31 to 0
That is, it only doesn't see ports in the range 32 to 63. If you try to use this interface with other I/O mapped devices, keep this in mind.

Despite these drawbacks, the arcade game players in our household are absolutely thrilled with the unit. I'll race the Kempston interface a 9 out of 10. A higher score would have been given for more complete decoding, as the unit works well and is quite good value for the money. (Mine cost £14 including Air postage. Kempston do take VISA, as well.

*The singular exception here may be Penetrator ** See back issue of LISTING for ways to do this.

HEADER WRITER

Here's a crude, but effective, technique for writing your own tape headers. When coupled with a header reader program (Nasir's, Syntax's etc.), you can create "phony" program headers with which you can fool your system into letting you see what is really going on, or load that "unmergeable" program.

What we need to do, particularly in the case of some autostart and \$MEM programs, is replace the autostart portion of the header with a code that doesn't say "autostart". We could write a machine code program to generate our ersatz header, but there is a simpler, and certainly quicker, way, particularly if we're dealing with a "loader".

In order to load screens and code, we must first load a BASIC program called "LOADER". The job of this program is to set up the system the way we want it (e.g., make it unmergeable) and then load the machine code and/or other BASIC blocks of our software. These "Loaders" are only long enough to do just that i.e., 100 to 200 bytes. All we need to do is make our own "dummy" program (e.g., 1 REM XXX...(100 or 200 X's) XXX, and save it's header to tape. Our dummy header will not have autostart, so the loaded program will be frozen before it can do any dirty work.

We must first, of course, find out how long the original loader was. We can use the header reader to do this and follow the instructions below:

- 1) Read original header-using header reader, and STOP the tape, immediately and take it out of the player.
- 2) Create Basic Program of same length as header says, but with NO AUTOSTART (use reader to check length - trial and error) Now Basic Loaders are short - typically 0 or 1 REM and 200 bytes or less. Your "Dummy" program can be just 1 REM XXX...XX. Just so long as it is the same length as the original.
- 3) Save New Dummy program (Header is all you really need) on a blank tape. Do a normal; SAVE "YES!"
- 4) NEW the machine
- 5) Now LOAD"" Just the header of the dummy program.
- 6) Stop the tape as soon as the dummy header has loaded.
- 7) Switch tapes - the original program should still be set just after the BASIC header, but before the actual code loads.
- 8) The computer is still waiting for the program, so play the original tape.
- 9) The program should now load, but not autostart, and you can examine and/or change it to your hearts content.

Alternate: make up a header that says the program is CODE. Then peek the values of the "CODE". They could even be poked (e.g., the 0 is \$ MEM, changed to a 1) and then resumed with a dummy BASIC header.

We assume most readers know that when you load a tape there is a 5 second tone, followed by a short (17 bytes) burst of code, called a header, which tells the 2068 what is coming next. There is then a brief period of silence followed by a 2 second tone and the program data.

Here's a short, direct, MC LOADER from Chuck Russell. We've used it to input Dick Scoville's "Printer Darkening Routine". You can see the results of this routine in the second decimal printout.

LOAD & RUN MCLoader and then follow the prompts. Your MC is stored in Strings B\$. RAMTOP will automatically be reset when you respond to the questions. This program is designed to fit your code right below ramtop, so if you specify a starting address you'll have to lower it manually. The HEX listing (3rd column) is from a more advanced version of the program (which is on LIE Tape 1.7).

To execute Scoville's routine simply RANDOMISE USER 57786.

[illegible]

HINTS & TIPS

Chuck R. offers this quickie - when you save a long program, enter

SAVE "NAME": BEEP X,Y

Where X & Y are #'s that suit your taste.
After the save, your computer will beep
to let you know it's done the job.

CLINICAL RELEVANCE

If your *W75* exploded state is not working, it may be because of poor contact between the keyboard's flat ribbon conductor and the microcram. I took the bottom 5 keys on the right side and recently for an apparent reason. A screw just under the *W75* schematic above these keys (A1, B1, C1, D1) is controlled by the 25-pin line on the 8 connector block. Apparently, the very thin metal film on the plastic card can be scraped off during assembly.

pull the ribbons out of their plugs and hold the ends up to a light. If the metal ribbons are scratched off, you'll see daylight through the tracks. Repair is simple, just take a sharp page of wire and trim exactly $\frac{3}{16}$ " from the bottom edge of the flat cable (don't forget the notch).

Diagnosing the problem is just as easy - if you loose columns (e.g., V,D,F,G and I, J,K,L) you've just contact with one of the DSD lines (in this case K) on the 5 pin connector. If you are lost (as in my case), you'll loose 5 legs, so suspect the eight pin legs.

P. Donnelly

RECONCILE

Here's Richard Cunningham's Bank Reconciler program. Use it to balance your checkbook and avoid those low balance fees. Instructions are in the program.

```

1 REM Program "bankrec"
2 CLS : BORDER 6 : INK 9
3 LET b=0 : LET c=0 : LET d=0
4 LET e=-1 : LET f$=""
5 POKE 23556,8
6 POKE 23557,100
10 PRINT PAPER 6 : "BANK RE
CONCILIATION
15 PRINT AT 1,1 : INK 7 : "turn o
n printer for hard copy: AT 1,0
OVER 1 : PAPER 4 : "
20 INPUT FLASH 1 : BRIGHT 1 : "
ENTER TODAY'S DATE
25 GO SUB 2000
26 BORDER 6
28 CLS : DEEP .2,15 : PRINT AT
0,0 : "CHECKBOOK RECONCILIATIO
N : AT 0,0 : OVER 1 : PAPER 4 :
INK 7 : "
29 PRINT INVERSE 1 : " REVIEW O
F CHECKBOOK ENTRIES
30 PRINT AT 3,0 : "Enter Pay/Deb
TAB 14 : "Deposit : TAB 25 : "BALA
NCE : AT 3,0 : OVER 1 : PAPER 5 : "
32 PRINT AT 2,3 : PAPER 6 : "DATE
: AT 2,15 : es : AT 2,0 : OVER 1 : "
35 PRINT AT 0,0 : "CHECKBOOK
RECONCILIATION : AT 0,0 : OV
ER 1 : PAPER 4 : "
36 PRINT INVERSE 1 : " REVIEW
OF CHECKBOOK ENTRIES
37 PRINT AT 1,15-LEN es/2-3 : "
Date : es : AT 1,0 : OVER 1 : "
38 PRINT AT 2,0 : "Enter Pay/Deb
TAB 14 : "Deposit : TAB 25 : "BALA
NCE : AT 2,0 : OVER 1 : PAPER 5 : "
40 INPUT "ENTER STARTING DATE/C
K# BEGINNING ENTRIES : " : es : PRI
NT AT 4,0 : PAPER 4 : "BEGIN ENTRIE
S (Dte/cck#) : es : "
41 PRINT AT 4,0 : PAPER 4 : "BEG
IN ENTRIES (Dte/cck#) : es : "
43 INPUT "1-ENTER BALANCE : " : b
44 LET b=INT (b*100+.5)
45 GO SUB 400
46 GO TO 120
50 INPUT INK 9 : PAPER 7 : "NEXT
ENTRY : PAPER 2 : "CK# : PAPER 7
OR : PAPER 1 : "DEPOSIT : P
APER 7 : "ENTER C OR d : " : m
FOR MENU : is
70 IF is="c" THEN INPUT INK 9 :
"Enter Amount of : PAPER 2 : "che
ck : c : "
80 IF is="d" THEN INPUT INK 9 :
"Enter Amount of : PAPER 1 : "DEP
OSIT : d : "
85 IF is="m" THEN CLS : GO TO
2000
90 IF is="c" AND is="c" AND
is="d" THEN GO TO 50
110 PRINT TAB 1,0 : " : $ :
120 IF is="c" THEN GO SUB 200 :
PRINT TAB 12-LEN c : $ :
122 IF is="d" THEN GO SUB 300 :
PRINT TAB 13 : " : $ : TAB 22-LEN d : d
123 GO SUB 400 : PRINT TAB 23 : "
: IF b=0 THEN PRINT TAB 32-LE
N b : $ :
125 IF b<0 THEN PRINT TAB 31-LE
N b : $ : INK 7 : PAPER 2 : " : b :
130 IF is="c" THEN PRINT TAB 1
: $ : TAB 10-LEN c : $ : c :
131 IF is="d" THEN PRINT TAB 1
: $ : TAB 20-LEN d : $ : d :
132 PRINT TAB 31-LEN b : $ : b :
140 LET c=0 : LET d=0 : LET f$=""
150 GO TO 50
160 LET c=INT (c*100+.5)
170 LET c$=STR$ c
180 IF c=0 THEN LET c$="000"
190 LET c$=c$ TO LEN c$-2 : "+" :
200 LET c$=c$ TO 1
210 IF VAL c$=0 THEN GO TO 252
220 LET c$=c$ (2 TO )
230 LET b=b-c
235 RETURN
240 LET d=INT (d*100+.5)
250 LET d$=STR$ d
260 IF d=0 THEN LET d$="000"
270 LET d$=d$ TO LEN d$-2 : "+" :
280 LET d$=d$ TO 1
290 IF VAL d$=0 THEN GO TO 352
300 LET d$=d$ (2 TO )
310 LET b=b+d
320 RETURN
330 LET e=e+1
340 LET b$=STR$ b
350 IF b=0 THEN LET b$="000"
360 LET b$=b$ (2 TO LEN b$-2) : "+" :
370 LET b$=b$ TO 1
380 IF VAL b$=0 THEN GO TO 500
390 LET b$=b$ (2 TO )
500 RETURN

```

```

2000 BORDER 6
2002 POKE 23556,8
2005 PRINT TAB 15-LEN es/2 : PAPE
R 4 : INK 7 : es
2010 PRINT : PRINT TAB 9 : PAPER
3 : "INSTRUCTIONS"
2020 PRINT : PRINT TAB 1 : "1- To
verify checkbook entries : "
2030 PRINT : PRINT TAB 1 : "2- To
reconcile Bank statement : " W
ith your checkbook : "
2050 PRINT AT 2,0 : FLASH 1 : BRI
GHT 1 : PAPER 2 : " PRESS APPROP
RIATE NUMBER
2060 IF INKEY$="1" AND INKEY$="2"
THEN GO TO 2050
2070 IF INKEY$="1" THEN GO TO 28
3000 CLS : BORDER 2
3002 LET f$=""
3005 LET b=0 : LET c=0 : LET d=0
3010 PRINT PAPER 3 : " STATEMENT
RECONCILIATION
3015 PRINT : PRINT : PRINT : "
STATEMENT RECONCILIATION
3020 PRINT AT 1,0 : "ENTER CHECKS
OUTSTANDING : CK# IN : " CHECKBOO
K BUT NOT ON STATEMENT : "
3023 PRINT INVERSE 1 : " CH
ECKS OUTSTANDING
3024 PRINT AT 2,0 : INVERSE 1 : "
CHECKS OUTSTANDING
3025 PRINT AT 1,3 : PAPER 6 : "DATE
: AT 1,15 : es : AT 1,0 : OVER 1 : "
3028 PRINT AT 1,15-LEN es/2-3 : "
Date : es : AT 1,0 : OVER 1 : "
3027 PRINT AT 3,0 : "CHECK #/date
AMOUNT TOTAL : AT 3,0 : OV
ER 1 : "
3028 PRINT "CHECK #/date AMOUNT
TOTAL : AT 4,0 : OVER 1 : "
3030 INPUT "ENTER CHECK DATE OR
NUMBER : " : PAPER 2 : "enter : " : w
hen finished : is
3035 IF is="f" THEN GO TO 4000
3040 PRINT is :
3041 PRINT is :
3050 INPUT "ENTER AMOUNT OF CHEC
K : c : "
3052 LET c=c+c
3055 GO SUB 200 : PRINT TAB 12 : " : $
: TAB 21-LEN c : $ : c :
3056 PRINT TAB 12 : " : $ : TAB 21-LE
N c : $ :
3060 GO SUB 400 : PRINT TAB 23 : " : $
: TAB 32-LEN b : $ : b :
3061 PRINT TAB 23 : " : $ : TAB 32-LE
N b : $ :
3090 GO TO 3030
4000 CLS : BORDER 1 : PRINT PAPER
3 : " STATEMENT RECONCILIATION
4001 PRINT : PRINT : PRINT PA
PER 3 : " STATEMENT RECONCILLAT
ION
4002 LET b=0 : LET d=0 : LET c=0
4003 LET d=0
4005 PRINT AT 1,0 : "ENTER DEPOSI
TS OUTSTANDING : DEP# IN CHECKBO
OK : NOT ON STATEMENT : "
4010 PRINT INVERSE 1 : " DEP
OSITS OUTSTANDING
4011 PRINT AT 1,0 : INVERSE 1 : "
DEPOSITS OUTSTANDING
4017 PRINT AT 2,3 : PAPER 6 : "DATE
: AT 2,15 : es : AT 2,0 : OVER 1 : "
4018 PRINT AT 1,15-LEN es/2-3 : "
Date : es : AT 1,0 : OVER 1 : "
4030 PRINT AT 3,0 : "DEPOSITS/DATE
AMOUNT TOTAL : AT 3,0 : OVE
R 1 : "
4040 PRINT "DEPOSITS/DATE AMOU
NT TOTAL : AT 4,0 : OVER 1 : "
4045 INPUT "ENTER DEPOSIT DATE O
R NUMBER : " : PAPER 2 : "enter : " : w
hen finished : is
4048 IF is="f" THEN GO TO 5000
4050 PRINT is :
4055 PRINT is :
4058 INPUT "ENTER AMOUNT OF DEPO
SIT : d : "
4059 LET d=d+d
4060 GO SUB 300 : PRINT TAB 12 : " : $
: TAB 21-LEN d : $ : d :
4061 PRINT TAB 12 : " : $ : TAB 21-LE
N d : $ :
4065 GO SUB 400 : PRINT TAB 23 : " : $
: TAB 32-LEN b : $ : b :
4070 PRINT TAB 23 : " : $ : TAB 32-LE
N b : $ :
4080 GO TO 4045
5000 CLS : BORDER 6 : PRINT PAPER
6 : " FINAL RECONCILIATION

```

```

5001 LPRINT : LPRINT : LPRINT :
INVERSE 1 : " FINAL RECONCILLI
ATION
5005 INPUT "ENTER FINAL BALANCE
ON BANK STATEMENT : " : b
5008 LET b=INT (b*100+.5) : GO SU
B 400
5010 PRINT "STATEMENT BALANCE : "
5011 IF b<0 THEN PRINT TAB 23 : "
PAPER 2 : BRIGHT 1 : " : TAB 25 : " : "
: TAB 32-LEN b : $ : b :
5012 IF b=0 THEN PRINT TAB 23 : "
: TAB 32-LEN b : $ : b :
5015 LPRINT "STATEMENT BALANCE : "
5017 IF b<0 THEN LPRINT TAB 23 :
INVERSE 1 : " : TAB 25 : " : " : TAB 32-
LEN b : $ : b :
5018 IF b=0 THEN LPRINT TAB 23 :
: TAB 32-LEN b : $ : b :
5019 LET d=INT (d*100+.5) : GO S
UB 310
5020 PRINT : PRINT "ADDITIONAL D
EPOSITS : " FROM CHECKBOOK : TAB 2
3 : " : TAB 32-LEN d : $ : d :
5021 LPRINT : LPRINT "ADDITIONAL
DEPOSITS : " FROM CHECKBOOK : TAB
23 : " : TAB 32-LEN d : $ : d :
5035 PRINT : "
5036 LPRINT : "
5038 GO SUB 410
5040 PRINT "TOTAL : "
5041 IF b<0 THEN PRINT TAB 23 : "
RIGHT 1 : PAPER 2 : " : TAB 25 : " : "
: TAB 32-LEN b : $ : b :
5042 IF b=0 THEN PRINT TAB 23 : "
: TAB 32-LEN b : $ : b :
5043 LPRINT "TOTAL : "
5044 IF b<0 THEN LPRINT INVERSE
1 : TAB 23 : " : TAB 25 : " : " : TAB 32-L
EN b : $ : b :
5045 IF b=0 THEN LPRINT TAB 23 :
: TAB 32-LEN b : $ : b :
5046 PRINT : "
5049 LPRINT : "
5050 LET c=INT (c*100+.5)
5055 GO SUB 210
5060 PRINT : PRINT "LESS CHECKS
OUTSTANDING : TAB 23 : " : TAB 32-L
EN c : $ : c :
5061 LPRINT : LPRINT "LESS OUTST
ANDING CHECKS : TAB 23 : " : TAB 3
2-LEN c : $ : c :
5065 GO SUB 400
5070 PRINT : "
5071 LPRINT : "
5080 PRINT : PRINT PAPER 5 : "FINA
L BALANCE : " : IF b<0 THEN PRINT
TAB 23 : BRIGHT 1 : PAPER 2 : "OD $
: TAB 25 : " : TAB 32-LEN b : $ : b :
5081 IF b=0 THEN PRINT TAB 23 : "
: TAB 32-LEN b : $ : b :
5085 LPRINT : LPRINT "FINAL BALA
NCE : " : IF b<0 THEN LPRINT TAB 2
3 : INVERSE 1 : "OD $ : TAB 25 : " : " : T
AB 32-LEN b : $ : b :
5086 IF b=0 THEN LPRINT TAB 23 :
: TAB 32-LEN b : $ : b :
5090 PRINT : "
5091 LPRINT : "
5100 INPUT "NOW ENTER CHECKBOOK
BALANCE : " : CB
5110 LET CB=CB*100
5112 LET b$=STR$ CB : GO SUB 420
5114 PRINT "CKBOOK BALANCE : TAB
32-LEN b$ : b$ :
5115 LPRINT "CKBOOK BALANCE : TAB
32-LEN b$ : b$ :
5117 IF b=CB THEN PRINT TAB 3 :
PAPER 5 : "O.K. CORRECT BALANCE :
: TAB 32-LEN b$ : PAPER 7 : b$ :
5118 IF b<CB THEN LPRINT TAB 3 :
INVERSE 1 : "O.K. CORRECT BALANCE :
: TAB 32-LEN b$ : b$ :
5120 IF b=CB THEN GO TO 5200
5123 LET b$=STR$ (b-CB) : GO SUB
520
5124 IF b=CB THEN PRINT PAPER 1 :
"CKBOOK BAL IS UNDER : " : TAB 32-L
EN b$ : PAPER 7 : b$ :
5125 IF b=CB THEN LPRINT INVERSE
1 : "CKBOOK BAL IS UNDER : TAB 3
2-LEN b$ : b$ :
5130 IF b=CB THEN PRINT PAPER 2 :
"CKBOOK BAL IS OVER : " : TAB 32-LE
N b$ : PAPER 7 : b$ :
5131 IF b=CB THEN LPRINT INVERSE
1 : "CKBOOK BAL IS OVER : TAB 3
2-LEN b$ : b$ :
5190 PRINT AT 20,0 : PAPER 5 : "Ad
ve balance should equal bal- and
e in your checkbook : "
5200 PRINT AT 20,0 : PAPER 5 : "B
al- ance should equal CKbook Bal :
5210 PRINT PAPER 4 : " PRESS ANY
KEY TO START AGAIN : "
5220 PAUSE 0 : CLS : DEEP .2,25 :
GO TO 2000

```

This is a sample output from Richard C's "BANKREC"

graphics

CHECKBOOK RECONCILIATION
 REVIEW OF CHECKBOOK ENTRIES
 Date: NOV. 5 84
 BEGIN ENTRIES (Dte/CHE) @ \$0.00

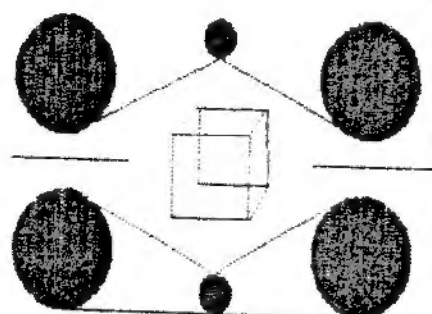
STATEMENT RECONCILIATION
 Date: NOV. 5 84
 CHECK #/date AMOUNT TOTAL

STATEMENT RECONCILIATION
 Date: NOV. 5 84
 DEPOSITS/DATE AMOUNT TOTAL

STATEMENT RECONCILIATION
 STATEMENT BALANCE: \$ 0.00
 ADDITIONAL DEPOSITS FROM CHECKBOOK: \$ 0.00
 TOTAL: \$ 0.00
 LESS OUTSTANDING CHECKS: \$ 0.00
 FINAL BALANCE: \$ 0.00
 CHECKBOOK BALANCE: 0.00
 BALANCE DIFFERENCE: \$ 0.00



Print & Plotter Paintbox did these



SCREEN DUMPS

Enter the world of "Micro-print"

Enter the world of "Micro-print"

Enter the world of "Micro-print"

Enter the world of "Micro-print"

Enter the world of "Micro-print"
 "Micro-print 85" Copyright Murnighan Software 1984.

Micro-print ... so what is it?

Micro-print 85 is an extra and separate printing routine for SPECTRUM's which releases you from the strict 32-pitch character format of Sinclair PRINT statements by offering you a constant choice of 32, 36, 42, 51, 64 and 85 characters/line pitch scripts in all the programs into which you add it.

The software you write can now display a great deal more text or numbers in the same screen space, with 5 new ultra-clear type-faces. This page is in "51", with a "42" heading: in "64" it looks like this ...

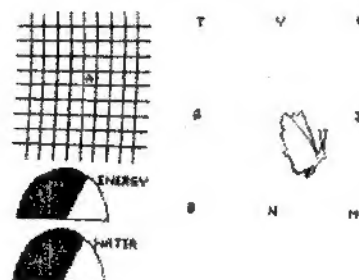
The programs you write can now display a great deal more text or numbers in the same screen space than they were able to before. This part is now in "64", and in "85" it's even more compact ...

The programs you write can now display a great deal more text or numbers in the same screen space than they were able to before. This part is now in "85" characters/line script, which is about as far as even three-point can go!



SPACE moves to next animal
 ENTER chooses this animal

Survival (Sinclair)



L_I_S_T GROUP

12/84 - 8

Technical Report:

To open (and close) Sinclair ZX-81 and Timex 1000 Computers

A note of caution: If terms are unfamiliar or confusing, the likelihood is very high that the reader should not attempt to even open the computer. You will probably damage something. The following comments are aimed at technical types who can assemble Heathkits and own items such as soldering irons, miniature phillips screwdrivers and are conversant with dikes, duckbill pliers and 3 terminal regulators.

1. To open computer, turn it on it's back and remove the three (3) rubber feet at upper left, bottom left and right. Then, using a #0 phillips screwdriver, remove the five (5) screws. Lift off the bottom of the case and set it to the side. Be sure to note which holes the 3 longer screws go to - - for re-assembly.
2. Now unscrew the two (2) phillips screws holding the P.C. board to the case top.
3. Carefully lift the P.C. board a slight distance above the case. At this time, you'll be able to see the flexible P.C. connectors which connect between the membrane keyboard and the P.C. board. While holding the P.C. board in one hand, gently grasp the flexible P.C. fingers - ONE AT A TIME - THERE ARE TWO - and pull them out of their sockets. You have now disassembled the computer to the point where you can work on it or inspect it.
4. Before re-assembly, carefully check the plastic flexible P.C. fingers, preferably with a loupe. If any damage - breakage, tear, creasing, hair-line cracks - the damaged portion must be trimmed away. Also, on the smaller of the two (5 lines), I always cut a portion of the plastic for ease of re-assembly (See drawing). *Page 4*
5. For re-assembly, you must use either flat nose pliers or duck-bill pliers for the first stage. If you don't, you will almost invariably damage one or more of the delicate P.C. lines on the flexible fingers - as they did at the Timex factory in Portugal - and sooner or later you will find that four (4) or more of your keys have ceased to function.
6. Lay the case top upside down with bottom facing away from you. (I always rest the case on my knee - upside down - with the case bottom tilted against a table or desk) Now, holding the P.C. board with left hand, position the P.C. board and case top like a book partially open, say 30 to 40 degrees.
7. Take your flat-nose or duck-bill pliers and grasp the smaller of the two flexible P.C. fingers (has 5 lines). Carefully insert it into the special connector like so: Using a light touch, introduce the plastic into the socket so it is just touching the entrance. Now re-position the pliers so you are grasping the flexible plastic 1/16 to 1/8" (NO MORE) outward from the socket. Finally, while holding the plastic vertical to the socket, carefully push it into the socket. Re-position the pliers outward again 1/16 to 1/8" and push the plastic in until you hit bottom. Use minimum force and do not bend or crack the flexible plastic.
8. Repeat above process with the larger (8 lines) flexible plastic connections.
9. Using your #0 phillips screwdriver, re-install the 2 screws to hold the P.C. board to the case top.
10. Now replace the case bottom. Remember to insert the longer screws above and the 2 shorter screws below.
11. To replace the rubber feet, use Scotch double-sided tape. Failing that, use some form of adhesive or cement.

12/84

Jess Peeher

SIMPLE T/S 2068 OUT PORT (Part 1)

Before your T/S 2068 computer can be used to perform real tasks instead of playing games or to carry out simple calculations it needs some means for actuating external devices. Our simple Out Port provides the interface between the 2068 and whatever external item you wish to control (turn 'on' or 'off').

The IC used in the Out Port circuit is a 74LS374. This circuit accepts data signals only when it is addressed to the clock input. The 'state' (high or low) is retained until the next clock pulse is sent to the 74LS374 IC. The IC will then be latched 'on'.

Addressing is accomplished by the use of two gates of a 74LS27 IC. Address 31 was selected because only one signal is required on A5 with \overline{WR} (write) and \overline{IOR} (in/out request). One gate of the 74LS27 accepts and decodes the address decode signal and the second gate inverts the signal. Inversion of the signal is required to trigger the port latch.

With the Out Port connected to the T/S 2068/Spectrum (it works with the Spectrum ROM) edge connector and 255 entered into location 64000, the following sequence would be carried out:

- 1- Basic Program: 10 OUT 31, PEEK 64000
- 2- 11111111 is the binary equivalent of 255 which appears on the data bus and the appropriate pins of the 74LS374 IC.
- 3- Signals A5, \overline{WR} and \overline{IOR} trigger gate 1 of the 74LS27 and direct it to latch all of the eight mono-stables (flip-flops) which outputs eight 1's to the eight output lines on the 74LS374.
- 4- The next basic command will remove the signals to the 74LS27 allowing the program to continue with other activities.
- 5- Since the 74LS374 is in a 'latched on' state, all eight outputs (eight 1's) will remain on until the next OUT 31 command is given. (OUT 31, 0 will command the 'off' state).

Testing the Out Port is simple. Solder a 270 ohm, 1/4 watt resistor to the anode lead of an LED (see diagram). The cathode lead of the LED is attached to the '0' volt terminal (ground).

Connect the Out Port to the 2068 edge connector (with POWER OFF). Turn the computer on. If the copyrite appears on the monitor screen, then everything is OK.

Attach the free end of the 270 ohm resistor lead to the 1'st output on the 74LS374 IC. Type: OUT 31, 1 and the LED should illuminate. OUT 31, 0 will extinguish the LED.

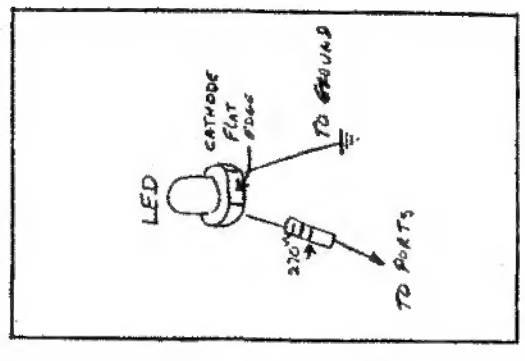
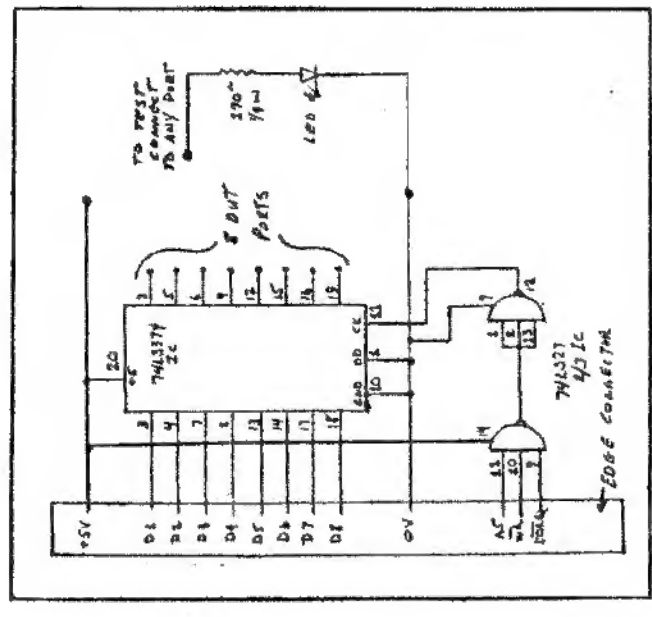
With eight LEDs and eight 270 ohm resistors attached; one to each Out Port and ground, a simple program will activate all ports: OUT 31, 255 or type: 10 FOR I=1 TO 255 : 20 OUT 31, (I) 30 PAUSE 10 : 40 NEXT I - This program will activate all eight ports and the LEDs will illuminate with the binary count (count 1 the first LED lights; count 2 the second LED lights; count three and the first and second LEDs illuminate ... and so on through count 255; all LEDs light).

The Out Port circuit is very simple to construct using a perf board, however a printed circuit board and/or a kit containing a PC board and basic parts is available from:
MAPLIN ELECTRONIC SUPPLIES, LTD.; P.O. BOX 3, RAYLEIGH, ESSEX, SS6 8LR ENGLAND
Printed Circuit board- LATCH CARD PCB 6832KBPS 2.50
PC board & Parts kit - LATCH PROJECT LK248.....BPS 6.50
BPS=BRITISH POUNDS STERLING.....MAPLIN accepts VISA card
MASTERCARD and AMERICAN EXPRESS CARD.
NOTE: The parts kit does not contain an edge connector.

- Parts required to build the Out Port
- 1- 74LS374 and 1- 20 pin IC socket
 - 1- 74LS27 and 1- 14 pin IC socket
 - 1- PC Board, Maplin, England or Perf Board, Radio Shack
 - 1- Edge connector, Zebra Systems, Inc (Zebra also offers an experimenters board; can be used in place of perf board)
 - 1- LED and 1- 270 ohm 1/4 watt resistor for testing output

Part 2, next issue of L.I.S.T. will cover isolating circuits to interface the Out Port and the outside world (relays and assorted opto-devices).

.....Bob Gilder



SCHEMATIC OF SIMPLE OUT PORT
(Mc Graw-Hill Book Co. UK Ltd)

LED TEST CIRCUIT

This project lets you install a 2764 EPROM into the existing 23 pin ROM socket found on most TS 1000's. It involves minor modifications to make the 2764 pinout compatible with the traces on the circuitboard. This project can also be done by trace cutting. However, the easiest way to make the modification is to install another 23 pin socket with the proper pins jumpered into the ROM socket. This method you can easily reverse your modification.

Pin 23 on the TS 1000 ROM is Address Line A16. A look at the pinouts on the 2764 reveals that pin 23 is reserved for A17. This is our first jumper. The A16 line of the 2764 is on pin 2 and this is already brought out provided for on your circuitboard. No modifications are necessary here. The only other change needed to complete the project is to bring ROMCE to pins 20 and 22 of the Epoch 500. This will be our second jumper.

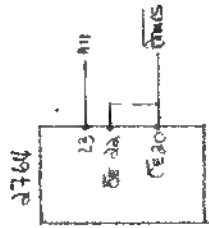
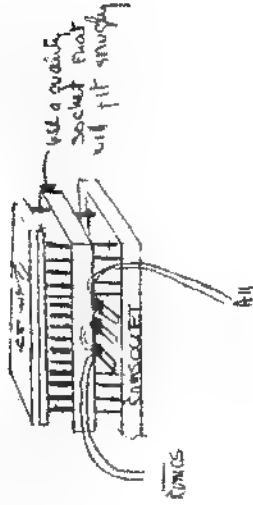
Only a few items are needed for the project: one 23 pin socket, some wire, wire and of course, a preprogrammed 2764. Bend socket legs numbers 20, 22 and 23 at a right angle to the socket body. Solder a jumper wire between 20 and 22. From here on, only two connections are needed to the TS 1000 circuitboard. Pin 23 will go to A16 and pin 20 to ROMCE.

You can pick up the ROMCE trace 22E 14E on the top side of the board by going to the 2nd pinthrough to the right of TPL. See Fig. 1. This is the pinthrough closest to the edge of the board. A16 trace 15E1 can be picked up on the cathode end of D1. This is the first keyboard diode and the first diode to the right of R47 on the top side of the circuitboard (Fig. 2).

With this configuration it is a simple matter to revise your ROM routines to suit your needs. Examples that come to mind are 64K Ramtop, Check on powerup, revised character generator tables and revised printer port patches. We would be interested in hearing from those who are experimenting with modified ROM routines.

CEM BRANT

TS 1000 EPROM/ROM Circuit Modification



TOP TS 1000

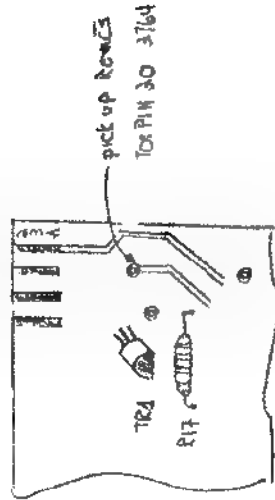


Fig 1

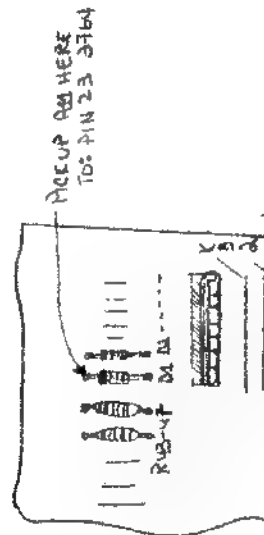


Fig 2

GROUP

LIST

TOM BENT
MD 21045, (301) 730-7187, has an upgrade for your ZX. Replace your 8K ROM with an improved eeprom. Full display file, SCROLL/CLS works properly, LPRINTs small numbers (.0001), DIM very large single strings (DIM A\$(47000)) in 84K and a few other nice changes are incorporated. \$20.00 for EPROM and circuit. (It fits under the hood.)

Switchboard

From Computer Living, NY

Switchboard is a listing by area code of those area bulletin board systems that Computer Living/News York knows about. If your bulletin board number is not listed here and you want it to be, call our BBS, the Computer Living Connection, at (212) 564-4796, and post the information there for Marc Roth, BBS sysop. Or drop us a line at our Manhattan office, 155 E 23rd St., New York, NY 10010.

NEW YORK (212)
 Paladins Palace/open/pwd/-
 Medevi (212) 699-0661
 ABBS (212) 896-0519
 Applesauce (212) 268-2062
 BAMS (212) 362-1040
 CONNECT 80 NYC (212) 991-1664
 Earth News Central (Apple II +)
 (212) 934-0774
 TPC ABBS NYC (212) 799-1577

The Database BBS (212) 772-7167
 TICKERSCREEN (212) 996-1600
 Rainbow Connection (NY)
 1 (212) 441-3755
 2 (212) 441-3766
 3 (212) 441-3719
 4 (212) 441-3907
 PMS McGraw Hill Brooks NY
 212-512-2000
 TCBS B.A.M.S. NY (212)
 362-1040*24
 PCRR/Gen/Open/Prgm (212)
 490-1146
 BBS/Sexual/Pwd (212) 341-5975
 BBS / Gen/Pwd/Prgm (212)
 775-1669
 AMIS/Gen/Pwd/Prgm (212)
 341-8463

AMIS/Gen/Pwd/Prgm (212)
 444-1434
 PMS/6-8/Shop/Open (212) 512-2488
 BMBBS Avenger's Mansion (212)
 534-2858
 Gramercy Modern (212) 684-3594
 NYKUG (212) 624-9148
 Suspended (212) 596-2660 Midnight
 to 8 A.M. only
 INE (International Network En-
 terprise) /Gen/Open (212) 795-5666
 718
 Omnimet/Open/Pwd/(718)
 837-2581
 Modem Over Brooklyn/Open/Pwd
 (718) 332-6470

516
 Lion BBS (516) 567-8267
 CONNECT 80/SOUTH (516)
 536-3510
 CONNECTION-80 (516) 586-5836
 CONNECT-80/Suffk (516) 924-8113
 Electric Mailman (516) 234-0925
 LICA CBBS (516) 361-6390
 MIDAS (516) 367-8619
 PIRATES COVE (516) 944-6712
 CONNECTION-80 Centereach
 (516) 582-5836
 ABBS Pirates Cove (516) 698-4008
 Adventure BBS (516) 621-9296
 TBBS/Stock/Open (516) 794-1707
 The Dragon's Lair (516) 374-3071
 Phosphor (516) 671-6195

BWARE:

These #'s change frequently

This is From LICA's BBS

Name	Telephone #	SYSOP	Specialty
RIBBS	201 272-1874	Bruce Katoff	
T.A.H.	201 376-8055	Brett Shack	Adventures
FORUM-BO	201 688-7117	Union, NJ	
ABBS ACNJ	201 757-1225	Tompton Plains	
PHOTO-BO	201 790-6797	Alan Hyman	Photography
AKHS CENJ	201 877-7228	Tompton Lakes	
CONNECT-BO	201 842-7644	Lincroft, NJ	6 Mid N.W. Sun
ABBS Wyckoff	201 891-7441	Ralph Koehrich	
ABBS ACNJ	201 968-1074	Dunellen NJ	
COMMUNIQUE-BO	201 992-4847	Andy Frisch	
J--			
AFROMS	203 272-2668	Dave Schwartz	
BULLET-BO	203 744-4644	Joe Simon	
J--			
BAMS	212 362-1040	Roger Kaplan	
CONNECT-BO/GNS	212 441-3755	Robert Rosen	Radio Shack
CONNECT-BO S.I.	212 441-3755	Staten Island NY	
IFC ABBS N.Y.C.	212 799-9777	Adrian Steckel	
HARDWARE HAVEN	516 328-8209		
PMS OF LI N.	516 724-2134	Richard Taylor	
L.I. S.E. TRS-RU	516 338-4067	Bill Raferty	
MIDAS	516 767-8619	Marc Mandel	
M. HUBARD	516 473-1005	Bill Michlers	
INNOV-BO	516 481-8991	R. Niederhoffer	
CONNECT-BO/SOUTH	516 536-3510	Stephen Cook	
LICA UNIT	516 541-7949		
LICA LINES CARS	516 561-6590	Dave Minott	
TELETHEND	516 581-8696	M.A. Leone	For Info
SCAMS ATARI BBS	516 586-9765	Rick Waterson	
CONNECTION BU	516 588-5836	T. Vander-Stouwe	
GULL BBS 64	516 681-0751	Joe Tomesone	
PIRATES COVE	516 698-4008		Apple User's
TENBAR ABBS	516 757-2227	R.K. Jiminez	On-line pgms
TELE-I IN	516 796-6454		
THE MONASTERY	516 825-2753	Chris Caputo	
IRK-LNW BBS	516 924-8115	Jim Edwardson	
PORT PC BBS	516 944-6712	Michael Krieger	IBM PC board
J--			
TELEPORT 64	914 623-4248	Michael Kleinert	
CONNECT-BO/S.P.	914 942-2638	Stony Point NY	

And, of course there's the Zebra BBS - (718) 296-2229

Computer Shopper Bulletin Board

AMIS, New York, 212 241 8955
 AMIS, New York, 212 568 0682
 BROWN BBS NY 212 933 9459
 BULLET-BO New York, NY 212 740 5680 *24
 COMM-80 Queens, NY 212 897 1392 *24
 CONNECTION 80 Manhattan, NY 212 991 1668
 CONNECTION 80 NY 212 441 3755 *24
 FIREFOX Computer World, NY 212 454 1462 *24
 NET WORKS New York, NY 212 410 0949
 NYBBBS BO NY 212 626 0375
 PALADIN'S PALACE, FOREM CBB Queens, NY
 212 699 0861 *24
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 Washington, NY 516 944 7007
 NET WORKS PIRATES' FREE 516 627 9048
 STAR TREK, NY 516 569 0539 *24 run on ATARI 3001200
 TBS Long Island, NY 516 781 1762
 TBS SOURCE Long Island, NY 516 475 6463 *24

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 DAY DRIVE CENTERPORT, NY 11721
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 NY 11802

USERS GROUPS

✓ The Long Island Sinclair Timex Group formed when Timex/Sinclair left the personal computer arena. The group has been growing steadily ever since and wants to attract other Timex Sinclair owners.

For example, did you know that Timex 2068 owners can place the Sinclair Spectrum read-only memory in their machines to convert them to run hundreds of British software titled Secretary-treasurer Paul Donnelly says he can supply these for less than \$20.

A monthly newsletter, meetings and a small library of public domain programs are available to members. For more information, send a self-addressed, stamped envelope to the group at P.O. Box 438, Centerport, NY 11722.

LONG ISLAND

LIST (Long Island Sinclair-Timex) GROUP
 10 Idle Day Drive
 Centerport, NY 11721
 For information send SASE
 LONG ISLAND COMPUTER
 ASSOCIATION
 P.O. Box 71
 Hicksville, NY 11802

Computer Living/NY

COMMUNICATIONS CORNER

Bob M. offers the following tips which seems to be a de facto standard for up and downloading information to BBS's. Use:

(CONTROL R) to open your buffer
 (CONTROL T) to Close it.

Dec. 10, 1984

Infoworld

WANTED: If interested in joining a users group for Timex/Sinclair computers on Long Island, drop me a post card, Heinz O. Henken, 9 Dartmoor Dr East Northport, NY 11731

July 1984

Byte

128

12

A CROSS-CORRELATION OF THE SPECTRUM ROM VERSUS TS2068 Part 1

Copy Right © Aug. 1984 by N.A. Pashtoon

As most of us have noticed , machine code (MC) programs written for the Spectrum rarely work on the TS2068 . TIMEX Corp. in an attempt to upgrade the Spectrum's performance, included extra Basic commands , sound chip support , joystick support and more importantly bankswitching capability , which resulted in a redesign of the machine . As a consequence software enhancements and embellishments were put in the Read Only Memory (ROM) . Because of these enhancements the code would not fill the original 16K ROM . As such all the cassette handling routines and the bank switching code had to be relegated to the 8K Extension ROM (EXROM) .

To facilitate software conversion between the two computers , a disassembly of the ROMs of the two computers must be on hand . An excellent disassembly of the Spectrum ROM as well as a step-by-step explanation of every MC instruction is available in the superb book " The Complete Spectrum ROM Disassembly" by IAN Logan and Frank O'Hara . The book is available from both Melbourne House and Zebra Systems .

I worked on the TS2068 ROM-EXROM disassembly and prepared a ROM Atlas , establishing the cross correlation of the various ROM routines, in July 1984 . In August '84 I sent a copy to SYNTAX for publication , which was accepted for publication . As to what issue will it appear in I am not sure . Any way , starting with this issue of LIST we will start publishing the ROM Atlas serially . So hold on to your copies of LIST . The header reader in the Nov. '84 issue is part of this series of articles .

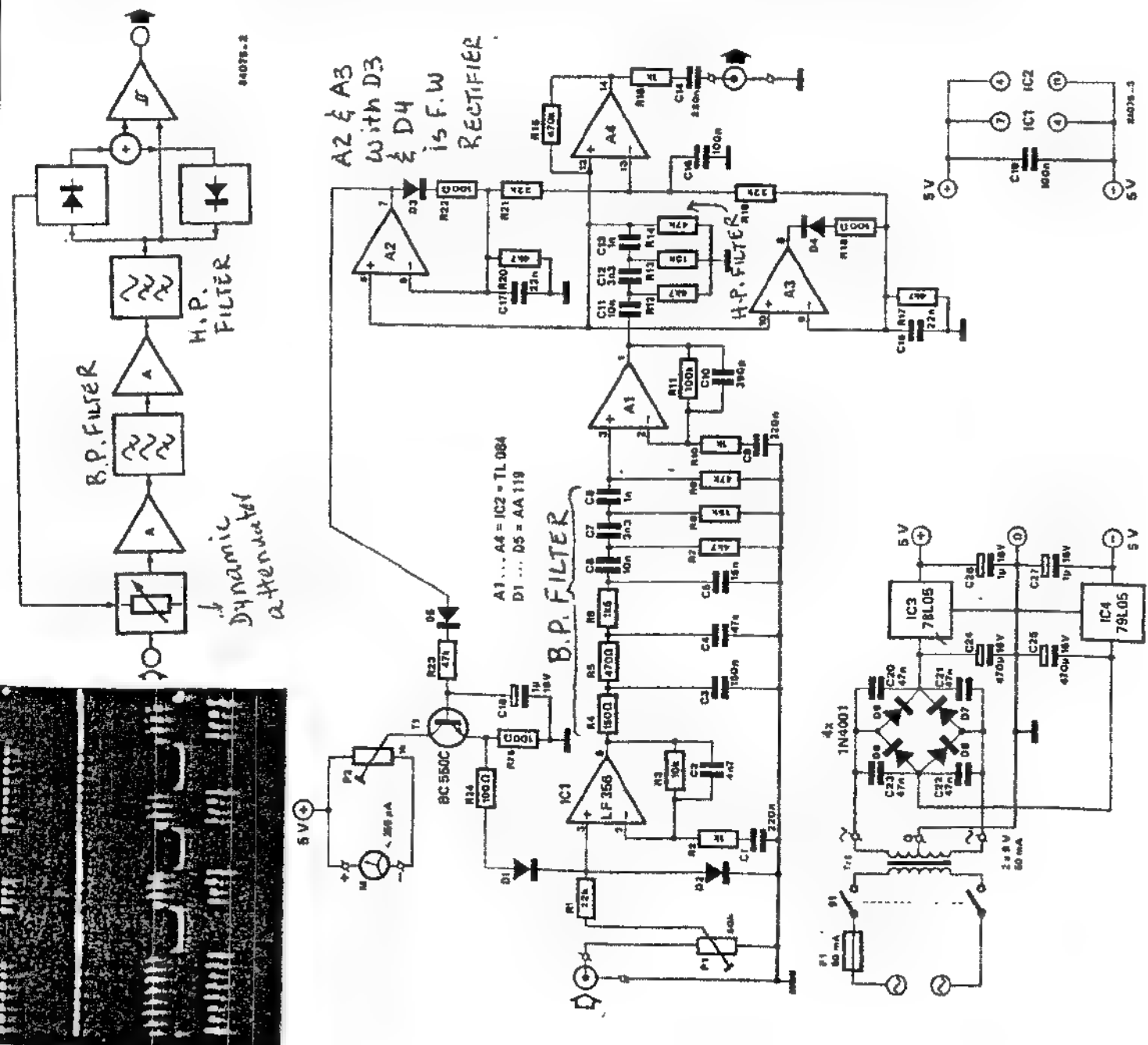
ROM ATLAS , COPY RIGHT N.A.PASHTOON

© AUG. 1984

SPECTRUM			TS 2068		
LABEL, NAME	ROM Addr	ROM NAME	LABEL, NAME	ROM Addr	TS 2068 LABEL, NAME
START	0000 0000	PLUCIN	CL-ATR	0E09	09C3
ERROR-1	0008 0008		CL-ADDR	0E98	09D6
PRINT-A-1	0010 0010	WRCH	COPY	0EAC	0AD2
SET-CHAR	0015 0018		COPY-BUFF	0EDC	0A23
HEAT-CHAR	0020 0020		CLEAR-PRB	0EDF	0A35
FP-CALC	0028 0028		COPY-LINE	0EF4	0A44
BC-SPACES	0030 0030		EDITOR	0F2C	0A82
MASK-INT	0038 0030		ADD-CHAR	0F81	0AE7
	004F 004F	PHLAF	ED-EDIT	0FA9	0B12
ERROR-2	0053 0053		ED-DOWN	0FF3	0B59
ERROR-3	0055 0055		ED-LEFT	1007	0B68
RESET	0066 0066	LE3	ED-RIGHT	100C	0B73
NO-RESET	0070 0070		ED-DELETE	1015	0B78
CH-ADD+1	0074 0074	MEIXCH	ED-ENTER	1024	0B84
TEMP-PT1	0077 0077	MC-HL	ED-EDGE	1031	0B97
TEMP-PT2	0078 0078	TC-HL	ED-UP	1039	0BBF
SKIP-OVER	007D 007D		ED-SYMBOL	1076	0BDC
TOKENS	0095 0098	TOKENS	ED-ERROR	107F	0BB9
KEYBL	0227 0243		CLEAN-SP	1097	0BFE
KEY-SCAN	028E 0280	K-SCAN	KEY-INPUT	10A8	0C0E
KEYBOARD	028F 02E1	UPD-K	ED-COPY	1110	0C83
K-REPEAT	0310 0336		REMOVE-FP	11A7	0D00
K-TEST	031E 035C	K-BASE	NEW	1107	0D10
K-DECODE	0333 0371	CHCODE	START/NEW	11C8	0D31
BEEPER	03B5 03F3	PARP	RAM-SET	1219	0D7F
BEEP	03FA 0436	BEEP	MAIN-EXEC	12A2	0E29
PRINT-OUT	04FA 0500	SEND-TV	MAIN-1	12A9	0E2F
PO-BACK-1	0A23 053A	P-LFT	MAIN-4	1303	0C08
PO-RIGHT	0A3D 0554	P-RT	REPORT-HSG	1391	0F65
PO-ENTER	0A4F 0566	P-NL	MAIN-ADD	135D	1158
PO-CUHA	0A5F 0576		CH-INFO	13AF	11AA
PO-QUEST	0A69 0580		INIT-STR	13C6	11C1
PO-TV-2	0A60 0584		WAIT-KEY	13D4	11CF
	0A9B 05B2	SET-AT	INPUT-AD	13E6	11E1
PO-ABLE	0A09 05F0		OUT-CODE	13EF	11EA
PO-STORE	0A0C 05F3	STTVCU	PRINT-A-2	13F2	11ED
PO-FETCH	0B03 061A	LDTVCU	CHAN-OPEN	1401	1230
PO-ANY	0B24 0638		REPORT-0	140E	123D
PO-ALL	0B7F 0684		CHAN-FLAG	1415	1248
PO-ATTR	0BDB 0710	ATTBYT	CHAN-K	1434	129A
PO-MSG	0C0A 073F	PUTRES	CHAN-S	1440	12A3
PO-SAVE	0C38 0776	PR-TV2	CHAN-P	1440	12B3
PO-SEARCH	0C41 077C		ONE-SPACE	1452	12B8
PO-SCR	0C55 0790	TVFUL?	MAKE-ROOM	1455	12BB
REPORT-5	0C86 07C1	ERR5	POINTERS	1464	12CA
TEMP5	0D40 0808	R-ATTS	LINE-ZERO	148F	131E
CLS	0D6E 0844	K-CLS	LINE-NO	1495	1324
CLS-LOWER	0D6E 0849	CLLMS	RESERVE	149E	132D
CLS-ALL	0DAF 08CA	CLS	SET-MIN	149E	133F
CL-SET	0DD9 0914	SET-CUR	SET-WORK	149F	134E
CL-SC-ALL	0DDE 0939	SCRL	REC-EDIT	149F	1363
CL-LINE	0DE4 097F	CLS-B	SET-STK	149F	1354

A ZX81 /TS1000/1500 PULSE CLEANER

In a recent issue of a general interest Dutch-based electronics magazine the following circuit was published . I think , I am reflecting the feelings of all ZX/TS users ,the dread with which we face the loading and saving problems with these machines . (One would think that Sinclair will learn from the experience of the Radio Shack TRS-80 Model 1 , but no ! they had to re-invent the wheel) . It is worth mentioning that the aforementioned Dutch magazine , in their past issues have addressed the problem of cassette loading for various makes of computers. Repeatedly , they have commented that these interfaces will not always work with some problematic tapes for the ZX81 . In other words , the designers for the magazine have been busy with this problem for a long time . The block diagram of the interface would hopefully explain the workings of the circuit . The circuit and then the power supply follows . Note that the one European transistor used can probably be replaced by a 2N2222 . The diodes D1,D2,D3,D4,D5 should be replaceable by 1N914 .



LETTER

TO LIST

LETTER RESPONSES

MCI Mail

November 26, 1984

If you'd like to help these people out and perhaps earn some "pen-pals", please volunteer for corresponding secretary, next meeting.

LIST GROUP
P.O. Box 438
Centerport, NY 11721-0438

About two weeks ago I bought the Zebra Graphics Tablet for the T/S 2068. It includes the Koala pad, interface and software from Zebra Systems \$119.95. I have only good things to say about this product. The hardware is first-rate, the software sophisticated and the manual is excellent. The 30 page manual is the best documentation I have ever seen. Most times I have to experiment quite a while with a new program and supplement the poor documentation. I had some trouble loading the tape, but then you see the first excellent feature of the software: Press B for backup tape. I always prefer to use my own tape. Here there is no complicated reading of header labels, etc. Just press B and you get your own tape from your own recorder. The software has too many features to list. There is no limit to what you can draw or paint, or type on the screen, copy to the 2040 printer, or save for later changes. You can even save your artwork for later use without loading the program again..

It's fun **** not marred by bugs.

Herbert A. Werthauer



Herbert A. Werthauer

9-27-84

Dr. B. B.
November 30 1984

1984 Listing Times Schedule Group
P.O. Box 438
Centerport, NY 11721
Dear Sirs:

Enclosed is a check for \$119.95 for the Zebra Graphics Tablet. I am enclosing a check for \$119.95 for the Zebra Graphics Tablet. I am enclosing a check for \$119.95 for the Zebra Graphics Tablet.

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Nov 19, 1984

Mr. Kurt R.
c/o List Group, P.O. Box 438, Centerport, N.Y. 11721-0438
Dear Kurt:

Recent receipt of the list group newsletter by our local informal group of T/REX-SINCLAIR users makes for some very interesting reading. Most of us operate one or more T/REX-SINCLAIR and various other times models. The last newsletter article indicates that you have successfully connected a Model 2068 to a Commodore 64 (costing for under \$70.00) to the T/REX-SINCLAIR. A group of us are wondering about this and would like to know where you obtained the hardware at that price and would it be possible to order some for ourselves by mail order. Would you be able enough to diagram and explain exactly what you modified to connect the Aquarius. Apparently, Daniel and I must have their electronics at this point of Canada and even if they did, the marketplace (compared to the New York area) would be very small and therefore not likely suitable for any kind of closeout pricing. Calgary is a city of about 600,000 people about 250 miles north of the Fort McMurray and is the center for oil and gas operations for Canada. Similar to Houston and Dallas in the U.S.A. I would appreciate very much an expansion of the newsletter feature that tells of recent bargain prices for any and all T/REX-SINCLAIR hardware, firmware and software as well as anything compatible (such as Aquarius) and complete names and addresses of any of these suppliers.

I have had personal experience with 27th Street Photo when I successfully ordered a T/REX-SINCLAIR package with software but I did not get the specific software requested. I currently have an order placed with Brooklyn Electronics for a spare T/REX-SINCLAIR and two 2040 printers. I haven't heard from them yet.

I also noticed from the newsletter that Chuck R. has been using a "portion monitor" (79.95) and likes it fine. I have rather like that but a 100 or even 200 but what an incredible low price.

I also would like to acquire a copy of the library type 1.0 if that is at all possible (for the T/REX-SINCLAIR). In the internal ROM/EEPROM button modification for the T/REX-SINCLAIR article, it appears that the author has only been using a ROM monitor that has a specific separator built in and so no electronics are needed. My experience however has been that all of the ROM monitors I have come across at a moment (but still fairly high) reasonable price need a separate vertical and horizontal sync signal from the computer as I have constructed successfully the electronic circuit shown in the T/REX-SINCLAIR TECHNICAL MANUAL and the resulting ROM display is superb. Hope I have not asked too many questions to offend your time. Please keep in touch if you are accessible. Address a 11 correspondence to Mr. D.S. (Kurt) Batterhill, 5208 Williams Crags, N.Y., Calgary, Alberta, Canada Postal Code T2A7T9.

Raymond J. Jones

ATTN: P.J. DONNELLY

CONF. NOUS.

NOVEMBER 14, 1984

JUST A QUICK NOTE WITH SOME HOPE IN THE T/REX-SINCLAIR OLIVEYI PRINTER. ALTHOUGH I AM DOING EXCELLENTLY WITH THE TASHMAN WORD PROCESSOR I HAVE BEEN UNABLE TO GET THE INTERFACE TO WORK WITH THE TASHMAN PROGRAM OR TO PRINT FROM MY OWN PROGRAMS.

I HAVE RECEIVED THE FOLLOWING INSTRUCTIONS FROM E. ARTHUR BROWN COMPANY FOR USING THE TASHMAN INTERFACE WITH THE OLIVEYI PRINTER. I TRIED IT AND IT WORKS.

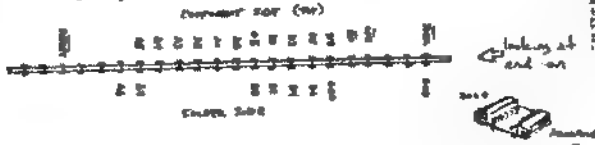
1. LOAD THE TASHMAN INTERFACE SOFTWARE AS USUAL.
2. ANSWER QUESTIONS FROM SOFTWARE AS USUAL.
3. SELECT "A" FOR BASIC TO STOP PROGRAM.
4. PORE 64763 & 64764 WITH 0 ZEROS.
5. HIT RUN TO RESTART PROGRAM.
6. ONCE AGAIN ANSWER ALL QUESTIONS BUT THIS TIME SAVE THE PROGRAM ON TAPE.

Best regards,

Raymond J. Jones

LIST GROUP

Here are your toys! Sorry about all the loose stuff, but it was packed in haste. The Commodore kids in the packs are worthless - CES has killed all Mafel accounts. You can bitch at them, but they again use/misuse to two and on for gas prices ever? Here's out-out -



I arrived two days earlier (wire-wrap) with pin bent down & core port. board, then wire-wrapped from the 30 pin to the Aquarius. That way, it can be debugged and then hard-soldered - be careful, it gets very confusing. The above connections map the 8251 at port 96h (data) and 97h (control). Internally, I have A0-A6 applied to an 8 input NAND and 97h A7 to the same gate thru a 8204 inverter. This drives the 8251's A0-A6. The 8251's A7 controls C/D line. 8251's signals are derived from two 'or' gates with 2040, 8204, etc. All else is obvious - all you need to do is complete the above connections and you are done - no counting! Modern use 8251 USER and the T/REX-SINCLAIR 300 baud random chip (worth \$25 + 11). A little on-board inverter supplies bias voltage. Not much else - it's very simple. I hope to have software done soon for 84 other terminal with buffers, etc. - I'll let you know. Don't try to take these apart - I had to use a saw to open one... they are so riveted together. If anyone else wants to write a simple terminal program, all setup is as per 8251 data book. Bit 7 of the STATUS bit indicates carrier detect, etc. - Good luck!

Kurt

How you saw 2068's at good price? (Commodore) .. I need another

25A to Vernon Valley Road, go south to Friendly's (pass the Post Office)
Go left on Alerton and then block to a "T" Intersection
Go left on Carrington and follow it to Dartmoor - 9 Dartmoor is shake shingled ranch,
just past the fire hydrant on the left.

SUNDAY JAN 6th 1984 - 2PM.

"MEMBERS ONLY" PAGE

LIBRARY TAPE #17 contains the following programs.

Program CIRCLE
Program FILEP
Program COTOROS
Program breathers
Program BAR CHART
Program MOIRE
Program word
Program CARRY
Program FOTCH
Program PRO TECH
Program EXCEPT
Program HEADER-3
Program CHARSET
Program ALTCHAR 1
Program ALTCHARSET
Program TRACE
Program CIRCUMBLR
Program ASSEMBLER
Program ASSEMBLER
Program MOON
Program DISAMON
Program MCUTIL
Program FENUM
Program RESVAL
Program TOMORROW

Program moonlander
Program blaste
Program moonrescue
Program galactoids
Bytes galactoids
Program tower
Program Apple
Program piano
Program sf10
Program 12345
Program plagun
Program t
Bytes b
Bytes b
Program BLACK MASK
Program CHECKERS

Program firstloar
Bytes firstloar
Program firstloar
Bytes firstloar
Program MEMOR
Program 139-demo
Bytes 139
Program expnd
Program circle
Program FILER

TS2068 Programming Tip

Faster Control

If you have a long text to edit
and you feel like your computer
is worn out before it starts, -OR
if your laser guns aren't firing
fast enough for you, --Try these
commands BUT BEWARE!!!

POKE 23561,0 (0 = 1 TO 35)
(I prefer 10 to 15 for Text)

POKE 23562,0 (0 = 1 to 5)
(I prefer 3 for Text)

For the touch typist this is
really a terrific advantage.

POKE 23561,0 (0 = 1 to 35)
(I prefer 1 to 10 for Games)

POKE 23562,0 (0 = 1 to 3)
(I prefer 1 for Games)



12/84

17

LL&T. Group

Here are two fine programs for the 2008 using several of the new toys. In our next issue we will present Guy's PHONEBOOK program. Buy Guy's address is: Buy Qualls, 4201 Diemitt Rd. #30, Plainview, TX 79072.

1. The first step is to identify the problem.

“我這本《中國通史》是根據我的研究心得，結合了許多人的意見而寫成的。其中不免有錯誤之處，但希望讀者能原諒。”

[illegible][illegible]

PLICIA **[28A]** **MEM** **SERIALS-1000**

Patentes for the T11000/Z111 Zebra Jeweltech Adapter.

Please call now to reserve these tickets with your friends. They were overlooked by customers like yourself. Important! There are now less than 100 left! Call for his cracking of the security compromised Times Fluor High-Speed Jetcon. It is by 2020.06.15 the month ended for 2020.

Other groups of major concern include the "American Indian Movement" as listed on the "National Indian Council on American Indian Affairs" and the "National Council on American Indian Affairs" and the "National Council on American Indian Affairs".

9 010' WALK
61 9491
62C 9491
71 9491
62C 9491
56 9491
60C 9491
68 9491
62C 9491
0 9491
9 9491
62 9491
SILBERT
LIMBERG

10/11/12
 10/11/12
 10/11/12

COMPLETION	DATE	REMARKS
17560	219	
17561	29	
17562	47	
17563	8	
17565	2	
17576	1	
17577	4	

[illegible]

67E-3884

12	6204E
32	9204E
64	\$204E
EE	8204E
9	1204E
102	2204E
111	*204E
EE8	D204
61E	6104
49	8104E
DAT	4104E
EE	910E
6EE	9104E
102	9104E
E	6104E
EE	7104E
9E	1+04E
92	0104E
504E	

12/84

18

TYPICAL WINKY BOARD



EFFECTIVE DATE CHANGED

Last minute problems have forced us to change the
 January meeting date to January 6, 1984, still at
 9 Dartmoor, Northport - 2:00PM.

THE TS2068 EQUIPPED WITH MICRODRIVES & EMU-1

N. A. PASHTOON

A note on Copy Rights: This article, as you may have noticed, does not bear a Copy Right notice. We believe that the wider dissemination of this type of information is in general public interest of TS users, in hopefully generating wider support for the TS2068 computers. As such, any TSUGs wishing to reprint this article, may do so, provided that due credit is given to LIST and the author.

Acknowledgements : I would like to thank Mr. Doug Dewey of the Triangle Users Group for supplying me (Sept. '84) with priceless information on TS2068 computer as well as the microdrives. Thanks are also due Mr. Roy Perschy, for an informative half hour exchange of ideas in mid October. That was the time, when both of us had zapped our Interface 1 and microdrive units in experimentation. Breaking the connection between the BE pin and ROMCS was his idea. Last but not least, thanks to our editor, Paul Donnelly, for cheerfully taking my calls at odd hours, and for his engaging technical discussions and ideas.

WARNING : The interfacing project described in the following paragraphs is an advanced project, in the sense that the connection of so many wires becomes very tedious and confusing. To safeguard the health of your computer, as well as your investment in the Spectrum Interface 1 unit and the microdrives, perform multiple checks before any physical connections are attempted to live buses. Especially be careful with the power connections. LIST and the author assume no responsibility or liability for any damage caused by your experimentation.

The Sinclair Spectrum computer, provides low cost, reliable mass storage on small continuous loop magnetic tape cartridges. The cartridges are theoretically capable of storing up to 125 kB of data (comparable to the storage capacity of the Commodore 64 disks). In practice, though, a sample of ten cartridges showed that the average storage capacity is more like 90 kB. The SAVING and LOADING of programs on the cartridge is achieved by a small cartridge drive mechanism with a read-write head and associated electronics, which Sinclair calls the microdrive. The microdrive and the Spectrum connect to each other via a black flat-lying box which Sinclair calls the Interface 1. The Interface 1 connects directly to the male edge connector on the back of the Spectrum, with the Spectrum resting on the forward sloping surface of the Interface 1.

Timex Corp., in their infinite corporate wisdom, decided to redesign the Spectrum, and make it into TS2068, also redefined the physical location of the various signals on the TS2068 edge connector. Both computers do have substantially similar signals on their buses, but there exist some differences too. As such, one cannot connect the Interface 1—microdrive combination directly to the TS2068 bus. In other words, an interface board between the TS2068 and the Interface 1 unit is required. Such an interface (henceforth called the TWISTOR) serves the purpose of alleviating the incompatibility problem that exist between the two buses.

The re-wiring scheme on the Twistor is illustrated in Fig.1. Fig.1 shows the 56 pin Spectrum bus with all the signals and power pins as it appears on the female edge connector on the Interface 1 unit. Any and all pins on this bus which requires a connection to the TS2068 bus is also shown in the diagram. The suffix T and B with any pin number refers to Top (component side) and Bottom (solder side); respectively, of a board. As an example, in Fig. 1, pin 27T which is A₁₀ on the Spectrum bus, should be wired on the Twistor, to A₁₀ of the TS2068 bus which is pin 19B. Any pins on the Spectrum bus, which do not have a parenthetical number indicated in the diagram should not be connected to any pin, for the purposes of this project.

The diagram in Fig.1 is sufficient for the construction of the Twistor except for the power connections. In order to reduce any confusion in wiring, and to facilitate double checking of the wiring, Fig.2 shows the TS2068 bus, pin numbers, signals, and power specs. For any pin which needs a connection to a corresponding point on the Spectrum bus, the connection is indicated parenthetically per the convention described before.

Now to power connections. In Fig.1, pins 22T, 23T and pin 4B, on the Spectrum bus, are shown as connected to a regulated supply. On my Twistorboard, I derived the appropriate power from the TS2068 bus pin 3B (Fig.2), through the use of a regulator. The voltage on pin 3B is nominally specified by Timex as +15 v, and is the raw DC from the power pack of the computer. The choice of the regulator was dictated by the available device in my junk box, namely a 12 v. 1.5 A rated LM340K-12. The circuit for this regulator and the connection pins on the Twistor are illustrated in Fig. 3.

Other parts needed for the Twistor is a perforated circuit board (Radio Shack 276-147 or the slightly smaller board), a 64 pin female edge connector (wire wrap type), a 56 pin male edge connector, and wire wrapping headers. The edge connectors you will use must have a spacing of 0.1 inches between the pins. It is important to note that the Interface 1 when it lies flat on a horizontal surface, has a forward sloping angle of approximately 15 degrees. This of course causes the female bus connector on the Interface 1 to have a similar angle with respect to the horizontal. The implication is that a rigid perforated circuit board with edge connectors on both ends could be problematic, to say the least, and at worst the stress on the computer board could damage the computer. I solved the problem by soldering an AP Products right angle strip header (#923878-R) on the perforated board, with a male edge connector soldered in between the pins, and satisfying the 15 degrees requirement. I also used AP Products straight wire wrap headers on the board to facilitate wire wrapping. It helps the wiring and double checking process tremendously to color code data bus, address bus, control bus, and the power distribution wires.

As mentioned in the beginning I zapped one Interface 1, not because of wiring errors, but because of careless measurements on 0.1 inch spacing wire wrap pins with my Sinclair DVM, while watching the display the probe slipped and shorted high voltage to a bus signal wire. I had to wait till Dec.6 to take delivery of a new Interface 1 and a 48K Spectrum. The moral of the story is to be extremely careful, especially with the power distribution wires. My suggestion is that you complete all the wiring first, except for power, triple check (or better yet, let some friend do the tracing), and then test the board with the TS2068, i.e. without the Interface 1 unit connected to the Twistor. Momentarily turn on the computer, and make sure that the copy right notice comes on the screen. Otherwise, turn off your computer immediately, and check your wiring till you find the source of the problem. After the Twistor passes the above test, complete the power connections on the board. Now insert your EMU-1 in the TS2068 cartridge slot, connect the Twistor and the Interface 1 and the microdrive to your computer. Start Praying (kidding). Turn on your computer. If everything is alright you will see the usual sequence of the copy right notices. Now insert a cartridge in the microdrive slot and type in RUN. You should get an error message MICRODRIVE NOT PRESENT... if you have inserted a blank cartridge. But if you have inserted, say, the sample programs cartridge, supplied with the drive, you will hear the pleasant and welcome purring of your microdrive. Sit there and be amazed and enjoy the speed of the device.

TS 2068 connections

INTERFACE 1

SPECTRA 1 BUS
 NC (208)
 NC (218)
 RFSR (268)
 MI (258)
 A₁ (208)
 A₂ (218)
 A₃ (228)
 A₄ (238)
 A₅ (248)
 A₆ (258)
 A₇ (268)
 A₈ (278)
 A₉ (288)
 A₁₀ (298)
 A₁₁ (308)
 A₁₂ (318)
 A₁₃ (328)
 A₁₄ (338)
 A₁₅ (348)
 A₁₆ (358)
 A₁₇ (368)
 A₁₈ (378)
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 A₂₀₇ (2268)
 A₂₀₈ (2278)
 A₂₀₉

FIGURE 1

INTERFACE 1 CONN.		TS2088 BUS	
1	GND	1	GND
2	SOUND	2	VIDEO
3	IOAS	3	BUSISO
4	BE	4	B
5	ROSCS	5	C
6	EXOR	6	R
7	RFSRB	7	NC
8	RI	8	A ₀
9	RESET	9	A ₅
10	BUSRQ	10	A ₆
11	VXIT	11	A ₇
12	BUSAK	12	A ₈
13	WRB	13	A ₉
14	RDB	14	A ₁₀
15	IORQB	15	A ₁₁
16	REQ	16	A ₁₂
17	HALT	17	A _{13B}
18	INT	18	A _{14B}
19	INT	19	A _{15B}
20	D ₀	20	A ₃
21	D ₃	21	A ₂
22	D ₅	22	A ₁
23	D ₆	23	A ₆
24	D ₂	24	CLK
25	D ₁	25	GND
26	D ₀	26	GND
27	SLOT	27	SLOT
28	DZIN	28	NC
29	D ₇	29	+5 V
30	A7B8	30	+15 V
31	EAR	31	TAPE
32	GND	32	GND

T = Comp. Side; B = Solder Side

FIGURE 2

... as an answer for the above mentioned test procedure . If during testing , or any other time , you get error messages like the above , with incorrect spelling and missing letters , turn off your system . The cause is improper connection on the edge connectors . Juggle both connectors side ways (gently please!) so appropriate contact is established .

Finally, if I were building the Twistor now, I will opt for using the Zebra Systems edge connectors and TS2068 prototyping board. I would cut the Zebra board so that it is just the width of the Interface 1 unit, with a male edge connector attaching it to the Interface 1 unit. I will use a cable between the Twistor and the TS2068. This way all the critical angling problems, and poor contact between edge connectors and buses will be solved. On the other hand if you wait a few weeks ("Real Soon Now": as Jerry Poughelle of Byte Magazine will say), there is a possibility of the announcement of a finished PC board. Keep checking your favorite BBs. (Please don't write to LIST or me, since we will not be involved in the venture).

As of the time of this writing, I am starting to test the RS232 port on the Interface 1. After I receive my first damaged Interface 1 from England, I plan to test the networking capability of the system. Any interesting developments will be reported in LIST.

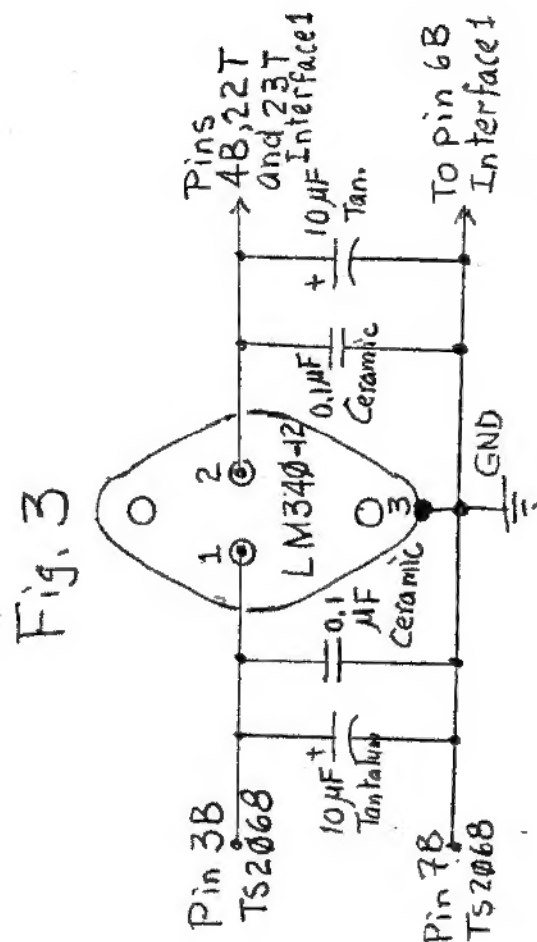


Fig. 3

HARDWARE NOTES:

A question as to whether the regulator is at all necessary is in order. If one opens a μ -drive (forgive me Sinclair!) one discovers a ULA, two transistors and a 78M05 regulator. The μ -drive bus reveals that raw DC is sufficient to operate the drive. The problem encountered when using the Interface 1 with the Timex computers, is that the power pack for the TS2068 delivers too high a voltage. The electrolytic capacitors in the μ -drive are rated at 16 v. The smoothing capacitor in the Spectrum power pack is also rated at 16 v. The service manual for the Spectrum specifies the absolute maximum peak of the ripple (no load, line voltage at 265 VAC) at 15 volts, and the minimum trough of the ripple (full load, line voltage 210 VAC) as 7.5 v. This means that the raw DC which for TS under no load conditions is nominally 22 v., is too much. Therefore the DC voltage level has to be lowered. What better solution than using a regulator IC? From the above it is clear that, for example, a 9 v. 1A will also do the job. The 1A rating will cater to your future expansions also. The +12 and -5 volts on the Spectrum are obviously required to accommodate the RS232 interface. But the RS232 interface is not so fussy (the voltages should have been +12 v. to start with) about voltage levels, then +9 and -5 v. is sufficient, where the -5 volts can be generated using either the Mostek DC-5 module, or the ICL7660, or a diode pump. In practice, the RS232 interface will even respond to +3 volts. The requirements for the networking option of Interface 1 are still unknown.

SOFTWARE NOTES

In order to get a disassembly of the Interface 1 8K ROM, you can copy the ROM into the system RAM. Type and RUN the following program:

```
10 CLEAR 32767
20 SAVE "M";1;"INT1 ROM"CODE 0,8192
30 LOAD "M";1;"INT1 ROM"CODE 32768
40 SAVE "INT1 ROM"CODE 32768,8192
```

The program copies the 8K ROM onto a μ -drive cartridge first, then loads the code in RAM starting at address 8000H, and finally SAVES a copy to cassette tape. If you have a Spectrum disassembler you don't need line 40. LOAD the disassembler. On the other hand use line 40 to get a copy on tape. Now using the TS2068 alone, use any of your favorite TS2068 disassembler. I have a disassembly, using the HOT Z-AROS cartridge (Oh! how very much I like this version of HOT Z).

The disassembly reveals that ports EF and F7 are used for control and status and other communications between the computer and the μ -drive. With the EMU-1 in the cartridge slot, for example, execution of the Basic line OUT 239,0 will turn on the μ -drive light, and the motor in the drive will start running. Surprisingly, when EMU-1 is not in the slot, the execution of the same line either from Basic or HOT Z will cause the TS2068 to freeze. (Does this mean that Russel's ROM Switch may not work?). This is definitely an interesting problem to look into.

The one disappointment, while studying the 8K ROM disassembly, was that the whole 8K ROM is full of code, except for 33 bytes which are not programmed. If some one out there in TS-land attempts to modify the 8K ROM code so that Interface 1 works directly with the TS2068 (without EMU-1 or ROM Switch) he/she will be facing a great challenge. The only person whom I can think of who very likely can manage it is Ray Kingsley (of HOT Z fame, and recently of the EXROM Corrector fame): RAY, HOW ABOUT IT? How about the reprogrammed EPROM being installed on an Oliger board in the cartridge slot? Obviously, the user will have to remove the existing 8K ROM from the Interface 1.

The interface 1 when connected to the system, causes the extension of the system's variables by 58 more bytes, defining new variables for use of the μ -drives, RS232, and networking. Of course, this will be in conflict with the TS2068 extension of the system variables area. The reprogramming job will be extensive. The channels and streams map's also extended.

The principle behind the operation of the Interface 1 unit is achieved by paging-in the 8K ROM. The Interface 1 8K rom overlays the lower 8K ROM of the system 16K ROM. The paging-in occurs whenever the 280 PC counter has a value of 8, i.e. when a RST08 happens. In the normal TS2068 and Spectrum environment RST08 is followed by a byte which determines what kind of error message the computer will print. With Interface 1 connected, whenever a RST08 is encountered it pages-in the 8K ROM in the interface. The ROM checks whether the error is the normal type of error, if so, the 16K ROM is switched back on and normal processing proceeds. But if the error was caused, say, through the use of any of the new μ -drive commands (syntax failure), the 8K ROM then processes that command, and then it switches itself off, enabling the 16K ROM.

The access to the routines in the Interface 1 8K ROM is provided by the byte following the RST08. This byte is known as a "hook byte". Hook bytes, ranging in value from 1BH to 32H provides such access. The hook byte 32H which Sinclair classifies as RESERVED, is interesting, because it enables one to get access to many 8K ROM routines, as well as achieve the paging of the 8K ROM.

Most of the routines in the 8K ROM uses all the main registers of the Z80 as well as HW, and IX. Thus these registers should be stacked for a safe entry and return.

Finally the manual provided with the Interface 1 is very good as far as the needs of the Basic programmers are concerned. Judging by the review of various books in the market, and having in my possession a book by Ian Sinclair and another by Andrew Pennell, I have to say that Pennell's book is an excellent reference work. It caters to the needs of both MC and Basic programmers. It has some nice MC routines, a good chapter on RS232, and a chapter on networking. The title and other info about the book follows:

Pennell, Andrew: "Master Your ZX Microdrive", Sunshin Books (an imprint of Scot Press Ltd.), London, England, 1983.

NOTE: Any feedback on the Interface 1 unit, RS232 interfacing, and networking from other TSUGs will be greatly appreciated. Please address your correspondence to the LIST P.O. Box.

The information that is supplied in the following paragraphs was compiled from "Your Computer" magazine, Pennell's book, The Microdrive Manual and some other documents.

THE RST XX IN THE INTERFACE 1 ROM

When you page in the Interface 1 ROM, the meaning of the Z80 restarts is different. You can't use the hook codes any more, but then you can call the subroutines directly. Also note, that even though the interrupts are on, the keyboard is not scanned and neither is FRAMES incremented. The restarts are as follows:

RST 0: Resets PLMS3 and returns you to the system ROM. (Unless I am mistaken, the TS2068 equipped with EMU-1 and the microdrives freezes when this restart is done.)

RST 8: Do not use this one.

RST 10: The two bytes following this RST are treated as the address of the system 16K ROM routine, so those routines can be called.

RST 19: Used for Syntax checking.

RST 20: The 8K ROM error check. The byte following it determines the type.

SOME INTERFACE 1 ADDRESSES AND HOOK CODES

Address	Name	Code	Function
0B81	232IN	1D	The input from RS232 in A.CY=valid
0C51	232OUT	1E	Output to RS232.Chan. code in A.
0DB2	WRITE_N	30	Write network packet.
0EA9	OPEN_N	2D	Open temp. "M" channel.
10C4	RECLAIM	2C	Reclaim the 595 bytes of "M" area.
11FF	WRITE_S	26	Send the "M" buffer to the drive.
12A9	CLOSE_M	23	Close the "M" channel.
17F7	MOTOR	21	For turning the motor on or off.
19A4	SHADOW	32	Call the Interface 1 ROM routine pointed to by the new system variable HD_11 i.e. (SCED).
19A8	NEWVARS	31	Generate the new Vars. if needed.
19D9	PAUSE	1B	Wait for a keypress.Value in A.
19EC	PRINT	1C	Print CHR\$ A to screen (stream FE)
19FC	LPRINT	1F	Print to printer (stream 3)
1A01	SCAN_K	20	Keyboard scan. NZ if pressed.
1A17	READ_P	27	Apriint buffer is read.
1A24	CLOSE_N	2E	Close an "N" channel.
1A4B	READ_R	28	Random file search.
1A86	READ_S	29	Serial file search.
1A91	WRITE_R	2A	For writing a block randomly.
1D6E	ERASE	24	For erasing files from the cart.

AFTERTHOUGHTS

1) Does any one out there in TS-Land remember a TIMEX technical memo #0003, dated 11/02/83. In that memo Timex referred to routines for Centronics Interface with both TS mode and CP/M mods. The invocation of these routines was through a service code which was supposed to be put after a RST 08. The amount of time that I wasted on that, and kept crashing the system left and right. It seems the concept is similar to the hook code concept for the microdrives. The service codes in that memo were utilized for the Function Dispatcher *later*.

2) Now being on the subject of the Function Dispatcher, every body will agree with me, that it is the most useless code. As Kingsley points out in the HOT 2-AROS documentation, if you know the entry point address to a routine why take the circuitous route and time consuming method of the Function Dispatcher? So why not get rid of the code for it in RAM, and opening the space for the new variables and channels which needs to be created for the microdrives?

Finally, Happy New Year to the inhabitants of the TS-Land. Happy μ-driving in 1985. NAP Dec.15, '84.